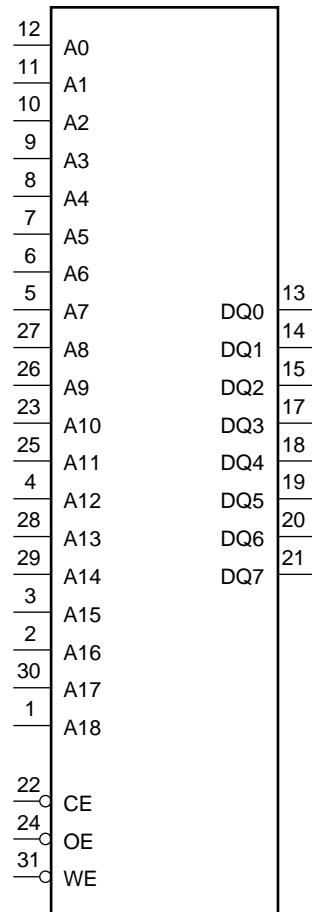
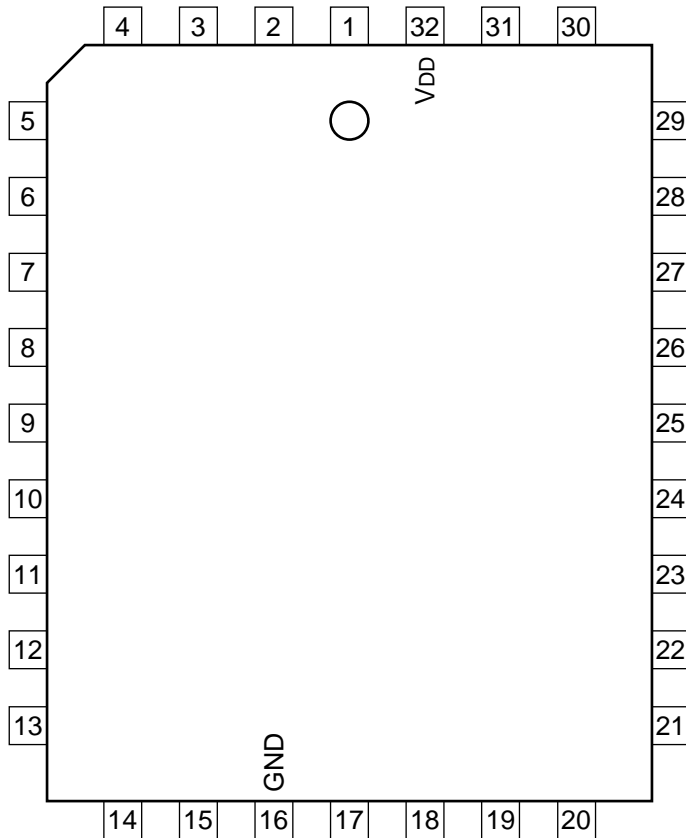


C-MOS 4 M (524,288 × 8)-BIT FLASH MEMORY

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	A18	17	I/O	DQ3
2	I	A16	18	I/O	DQ4
3	I	A15	19	I/O	DQ5
4	I	A12	20	I/O	DQ6
5	I	A7	21	I/O	DQ7
6	I	A6	22	I	$\overline{\text{CE}}$
7	I	A5	23	I	A10
8	I	A4	24	I	$\overline{\text{OE}}$
9	I	A3	25	I	A11
10	I	A2	26	I	A9
11	I	A1	27	I	A8
12	I	A0	28	I	A13
13	I/O	DQ0	29	I	A14
14	I/O	DQ1	30	I	A17
15	I/O	DQ2	31	I	$\overline{\text{WE}}$
16	—	GND	32	—	VDD

INPUT

A0 - A18 : ADDRESS INPUTS

 $\overline{\text{CE}}$: CHIP ENABLE $\overline{\text{OE}}$: OUTPUT ENABLE $\overline{\text{WE}}$: WRITE ENABLE**INPUT/OUTPUT**

DQ0 - DQ7 : DATA INPUT/OUTPUT